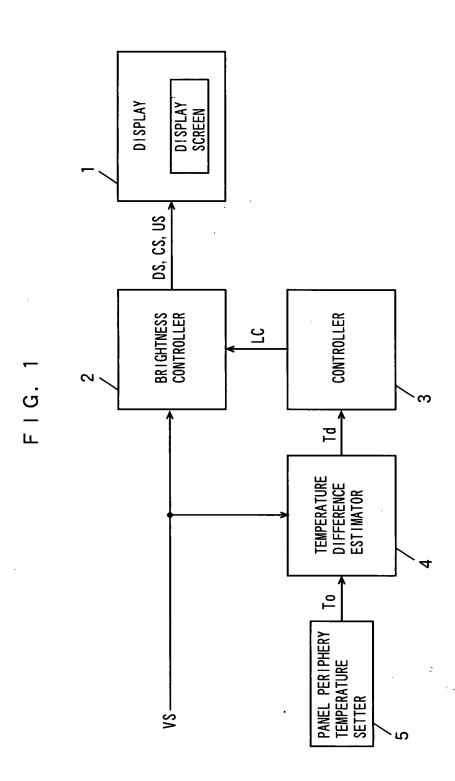
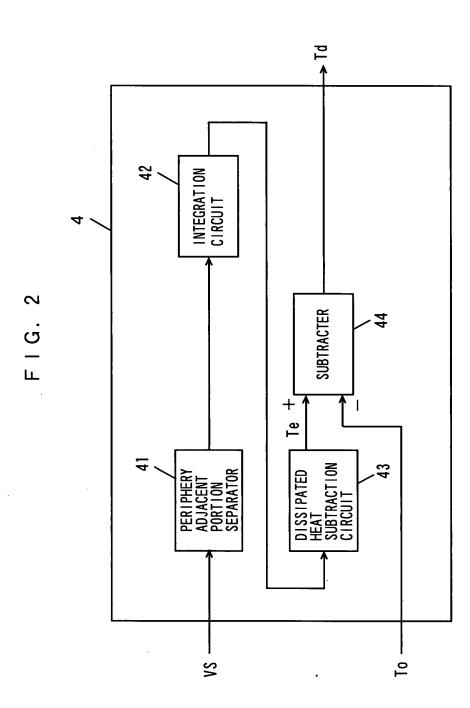
6414660

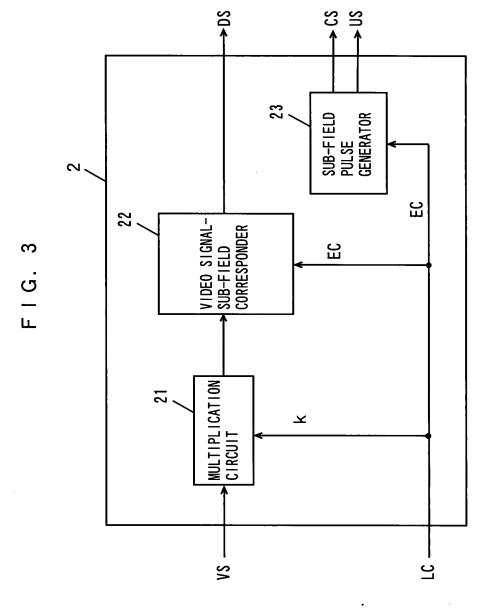


19 b'og

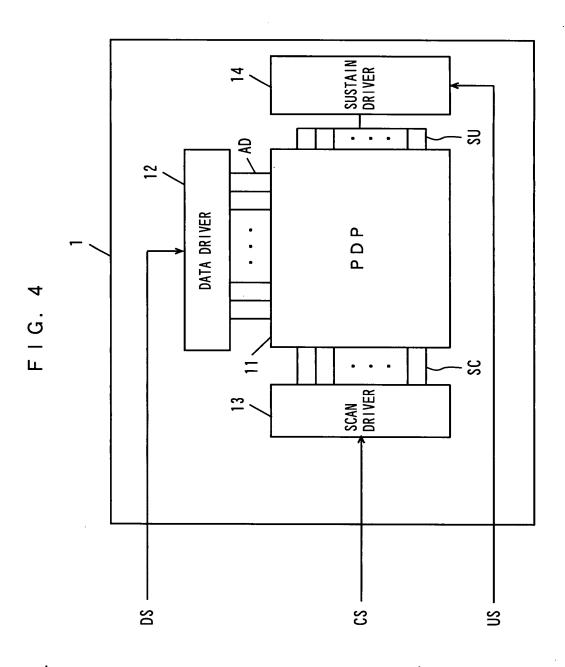


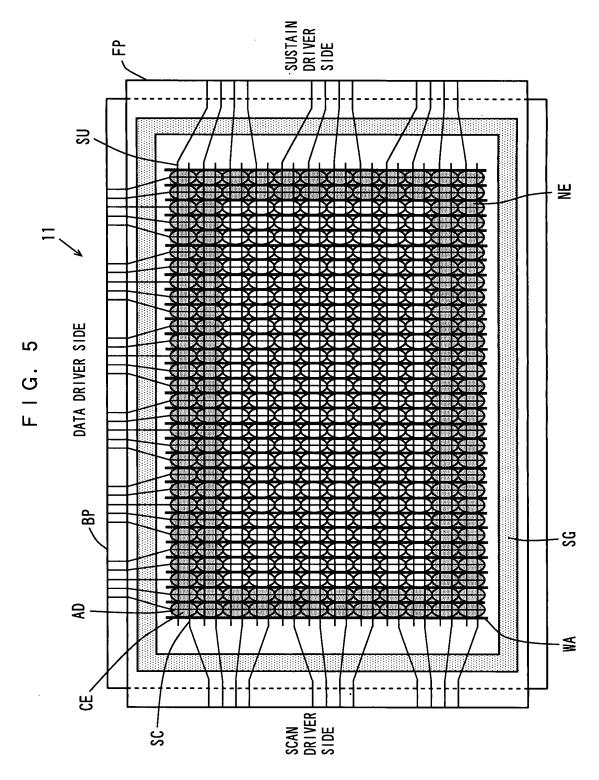


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F I G. 6

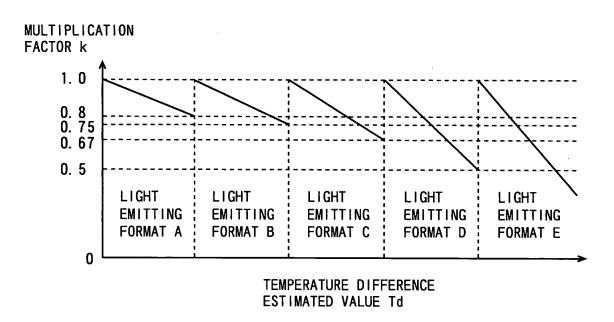
GRAY SCALE LEVEL	SF1	SF2	SF3	SF4	SF5	SF6	SF7	SF8
0								
1	0						-	
2		0						
3	0	0						
4~7	SAME A	S 0∼3	0					
8~15	SAM	E AS O	~7	0				
16~31	;	SAME AS	0~15	j	0			
32~63		SAME	AS O	~31		0		
64~127		S	AME AS	0~63			0	
128~255			SAME	AS 0~	-127			0

FIG. 7

## NUMBER OF LIGHT EMITTING PULSES

LIGHT EMITTING PULSE CONTROL SIGNAL	SF1	SF2	SF3	SF4	SF5	SF6	SF7	SF8	TOTAL
LIGHT EMITTING FORMAT A	5	10	20	40	80	160	320	640	1275
LIGHT EMITTING FORMAT B	4	8	16	32	64	128	256	512	1020
LIGHT EMITTING FORMAT C	3	6	12	24	48	96	192	384	765
LIGHT EMITTING FORMAT D	2	4	8	16	32	64	128	256	510
LIGHT EMITTING FORMAT E	1	2	· 4	8	16	32	64	128	255

F I G. 8



F I G. 9

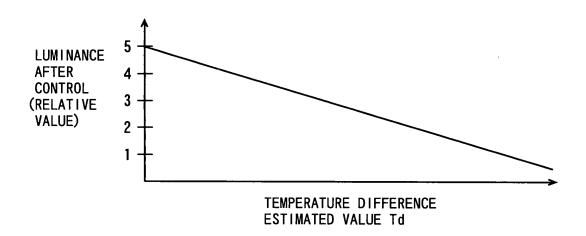
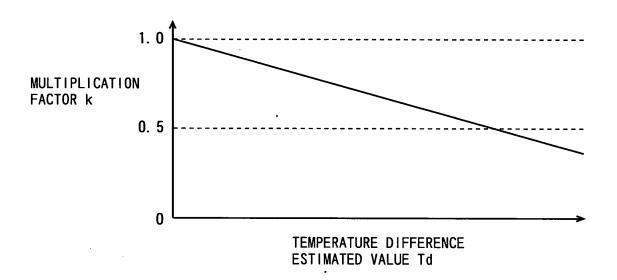


FIG. 10

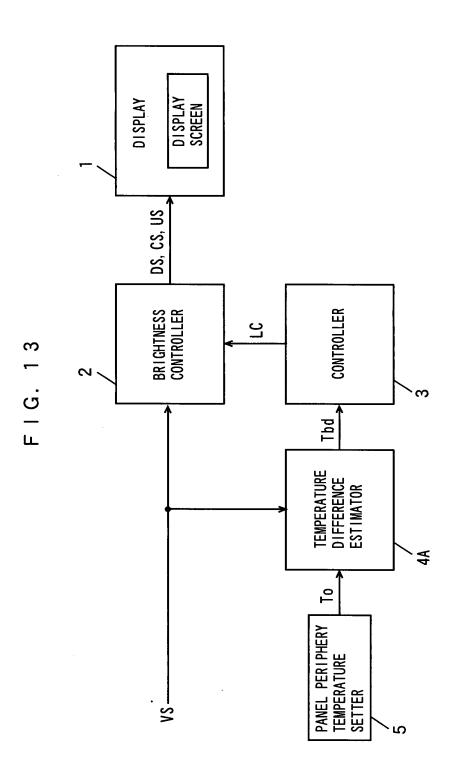




0. 5	0. 5	0. 5	0. 5	0. 5
0. 5	1	1	1	0. 5
0. 5	1	1	1	0. 5
0. 5	1	1	1	0. 5
0. 5	0. 5	0. 5	0. 5	0. 5

F I G. 12

0. 5	0. 5	0. 5	0. 5	0. 5
0, 5	0. 75	0. 75	0. 75	0, 5
0. 5	0. 75	1	0. 75	0. 5
0. 5	0. 75	0. 75	0. 75	0. 5
0. 5	0. 5	0. 5	0. 5	0. 5



INTEGRATION CIRCUIT SUBTRACTER BLOCK SEPARATOR **1** 

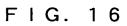
**P** Tbd

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FIG. 15

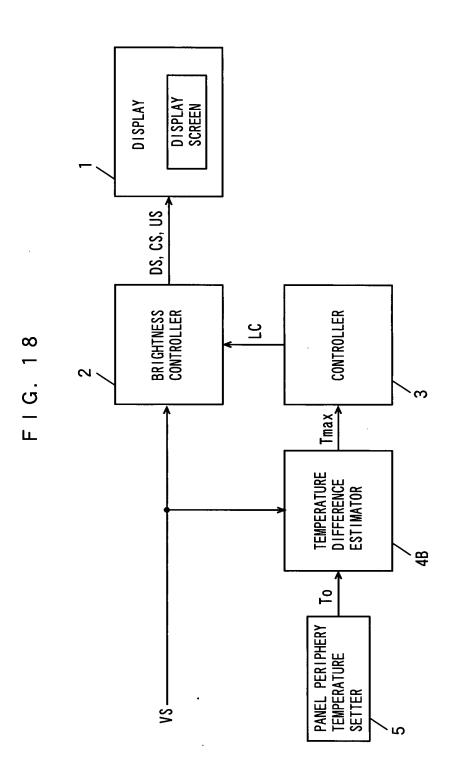
	17	18	20	15	17
	15				17
(a)	12				12
	8		•		10
:	18	15	16	5	8
į					<del></del>
į	· · · · · · · · · · · · · · · · · · ·	Q	10		7
	7	8	10	5	7
(b)		8	10	5	
(b)	5	8	10	5	7



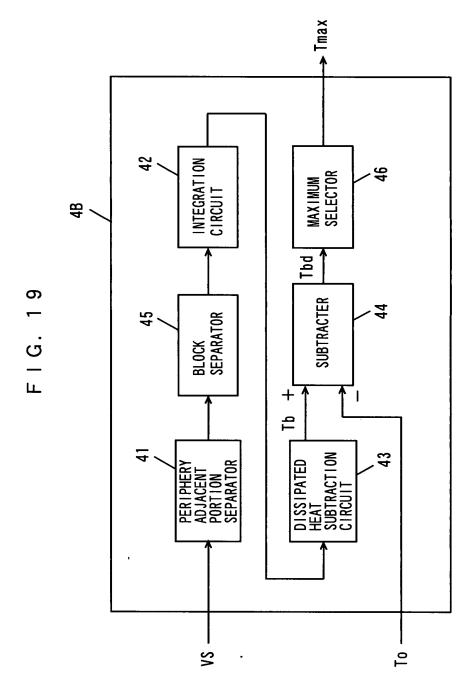
	О	0	20	0	0
	О				0
(a)	20				21
	О				0
	0	0	20	0	0
	· · · · · · · · · · · · · · · · · · ·	•			1
	1	1	0. 5	1	1
	1	•			1
(b)	1 0. 5	•			1 0.
(b)	1	•			1

FIG. 17

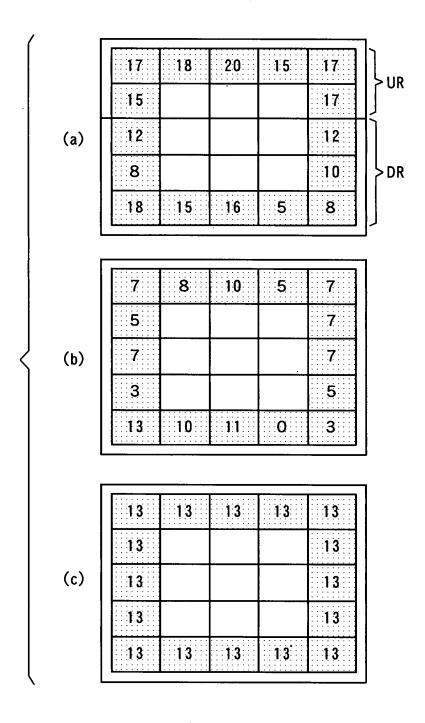
		_
	0 0 20 0 0	
	0 0	: :
(a)	20 20	: :
	0 0	: :
	O O 20 O O	
		_
	O 10 20 10 O	- : :
	10	: :
(b)	20 20	: :
(2)	10	: : : :
	O 10 20 10 O	: :
		_
	1 0. 75 0. 5 0. 75 1	
	0. 75	5
(c)	0. 5	<del>: :</del> : : : :
	0. 75	<u>.</u>

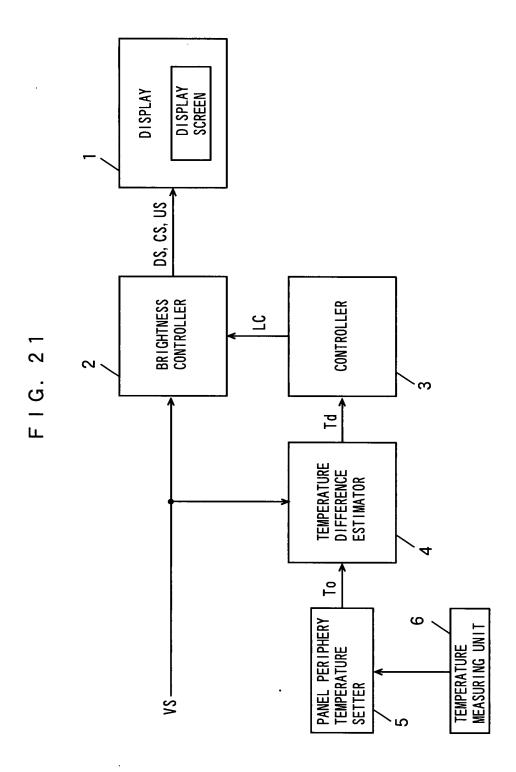


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F I G. 20





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